

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b), 1.97(c) or 1.97(d))**

Docket No.  
P15069-US1

In Re Application Of:

Serial No 10/517,181	Filing Date 12-07-2004	Examiner Chikaodili E. Anyikire	Group Art Unit 2621
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**Title**

Phase Locked Loop Circuit, Electronic Device including a Phase Locked Loop Circuit and Method for Generating a Periodic Signal

Address to:

Commissioner for Patents  
P.O. Box 1450, Alexandria, VA 22313-1450  
Fax no. (571) 273-8300

**37 CFR 1.97(b)**

The Information Disclosure Statement submitted herewith is being filed (1) within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); (2) within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; (3) before the mailing of a first Office Action on the merits; or (4) before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b) and before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

(1) Each item of information contained in the Information Disclosure Statement submitted herewith was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement submitted herewith; or

(2) That no item of information contained in the information disclosure statement submitted herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement submitted herewith was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement, or

the fee set forth in 37 CFR 1.17(p).

**37 CFR 1.97(d)**

The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(c), and on or before payment of the issue fee and is accompanied by

the statement specified in 37 CFR 1.97(e); and

(1) Each item of information contained in the Information Disclosure Statement submitted herewith was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement submitted herewith; or

(2) That no item of information contained in the information disclosure statement submitted herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement submitted herewith was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement, and

the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT  
(Under 37 CFR 1.97(b), 1.97(c) or 1.97(d))**

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**Payment of Fee**

The Director is hereby authorized to charge and credit Deposit Account 50-1379 as described below.

Charge the amount of \$ \_\_\_\_\_

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Reg. No. 40,542  
Ericsson Inc.  
6300 Legacy Drive  
M/S EVR 1-C-11  
Plano, TX 75024  
972-583-5799  
roger.burleigh@ericsson.com

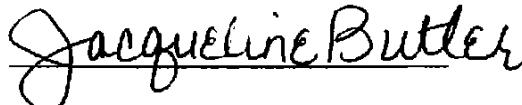
Dated: December 14, 2006

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First class or Express Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or being facsimile transmitted, or transmitted via EFS-Web (Beta) to the USPTO, on the date indicated below.

Date: December 14, 2006

Name: Jacqueline Butler

Signature: 

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<b>U. S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>			
<i>Complete if Known</i>			
Application Number		10/517,181	
Filing Date		December 7, 2004	
First Named Inventor		Johannes Wilhelmus Eikenbroek	
Art Unit		2621	
Examiner Name		Chikaodili E. Anyikire	
SHEET	1	OF	1
		Attorney Docket Number	
		P15069-US1	

FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation Y/N
	AA	WO 9531861 A	11-23-1995	Copeland Miles Alexander, et al.	

NONPATENT LITERATURE DOCUMENT			
Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
	AB	Beards R D, et al., "An Oversampling Delta-Sigma Frequency Discriminator" IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, IEEE Inc., New York, US, vol. 41, no. 1, 1994, pages 26-32, XP000439300 ISSN: 1057-7130	

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.